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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Thomas R. Bayerl, et al.

Serial No.: 09/745,919

Filed: December 21, 2000

For: Use of Bus Hold to Prevent Bus Contention
Between High Speed Processor and
Slow Peripheral

§ Group Art Unit: 2181

§ Examiner: Dang, Khanh NMN

§ Confirmation No.: 6827

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Mail Stop Issue Fee
Commissioner for Patents
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Alexandria, VA 22313-1450

I hereby certify that this correspondence and PTO Form 1449 with citations attached, is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on:

4-5-2005
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Edith Shek
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LETTER TO THE OFFICIAL DRAFTSPERSON

Attached please find formal drawings for this application, consisting of one (1) sheet, Figures 1 to 2, to replace the informal drawings originally filed. Each sheet of drawings indicates the identifying indicia suggested in §1.84(c) on the front of the drawing.

Respectfully submitted,

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